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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2811
Examiner: Thomas J. Magee

In Re PATENT APPLICATION Of:

Applicant: Hiroaki Nakano)
Serial No.: 10/756,366)
Filed: January 14, 2004) AMENDMENT
For: CIRCUIT BOARD FOR MOUNTING)
A SEMICONDUCTOR CHIP AND)
MANUFACTURING METHOD THEREOF)
Attny Ref.: TAI 146)
January 25, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is in response to the Official Action mailed on October 28, 2004. No fee is due. However, please charge our Deposit Account No. 18-0002 if any fees are needed to enter this paper, and please advise us accordingly. It is noted that no petition is required because of the authorization to charge, but please consider this paper a petition for extension of time if needed.

The independent claims 1 and 6 are amended. New claim 16 is supported in original claim 1, new claim 17 is supported in the drawing, and new claim 18 is supported in original claim 9. Claim 19 is supported in Fig. 1. The new claims are patentable for the reasons below.

The specification is amended to support the particular language of the amendments and the new claims.

FEE ENCLOSED: \$0
Please charge any further
fee to our Deposit Account
No. 18-0002